

Due to the COVID-19 crisis, the information below is subject to change, in particular that concerning the teaching mode (presential, distance or in a comodal or hybrid format).



5 credits	30.0 h + 30.0 h	Q2
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Teacher(s)	Bol David ;Flandre Denis ;
Language :	English
Place of the course	Louvain-la-Neuve
Main themes	<p>The world we live in is getting more and digital with electronic embedded systems surrounding us and communicating with the cloud. However, the physical world is analog in essence. The digital embedded systems thus need analog functions to interact with the physical world, its users, the cloud, the energy sources, as well as between themselves. This is done through sensors, actuators, user interfaces, power management units, wireline and wireless communications. Digital systems also rely on key analog functions performed internally for efficient operation: memories, clocking and voltage regulation. In this course, we study the architecture of the key analog electronic systems performing these functions.</p> <p>Within the ELEC/ELME formation, this course presents analog system architectures as a complementary to ELEC2531 course on digital system architectures. It serves as a basis for the courses on integrated-circuit synthesis (ELEC2650, ELEC2570 and ELEC2620).</p>
Aims	<p>a. <u>Contribution of the activity to the learning outcomes of the program</u></p> <ul style="list-style-type: none"> <li>• AA1 Knowledge base : electronic concepts (AA1.1), simulation and CAD tools (AA1.2-3).</li> <li>• AA2 Engineering skills: problem analysis (AA2.1) and solution comparison (AA2.3).</li> <li>• AA3 R&amp;D skills : find appropriate references on the existing solutions in the field of the flipped class topic (AA3.1).</li> <li>• AA5 Communication skills: oral communication (AA5.3, AA5.6).</li> <li>• AA6 Professional skills: use of appropriate standards (AA6.1), critical evaluation of technical solutions (AA6.3) and autonomous learning (AA6.4).</li> </ul> <p>b. <u>Learning outcomes</u></p> <p><b>After this course, the electrical engineers in circuit and systems should be able to:</b></p> <ul style="list-style-type: none"> <li>• identify the key performance metrics of an analog function in a given application context,</li> <li>• explain the operation of typical analog system architectures,</li> <li>• qualitatively model the performance with respect to the architecture,</li> <li>• evaluate the performance of typical analog system architectures with SPICE simulations.</li> </ul> <p>-----</p> <p><i>The contribution of this Teaching Unit to the development and command of the skills and learning outcomes of the programme(s) can be accessed at the end of this sheet, in the section entitled "Programmes/courses offering this Teaching Unit".</i></p>
Evaluation methods	<p><b>Due to the COVID-19 crisis, the information in this section is particularly likely to change.</b> The evaluation is based on the group challenges during the semester and an individual written exam.</p>
Teaching methods	<p><b>Due to the COVID-19 crisis, the information in this section is particularly likely to change.</b> The course is organized, In presence, as long as sanitary rules allow, as follows:</p> <ul style="list-style-type: none"> <li>• lectures on generic analog concepts and building blocks,</li> <li>• exercise sessions on these concepts and building blocks,</li> <li>• flipped classes about typical analog applications and associated specific architectures of analog systems, these classes are based on a reading at home and a group challenge in class with a SPICE simulation tool,</li> <li>• seminar given by an expert from the industry (if time allows).</li> </ul>
Content	<ul style="list-style-type: none"> <li>• Noise in analog circuits</li> <li>• Opamp-based circuits</li> <li>• Analog filters</li> <li>• Voltage and current references</li> <li>• Voltage regulators</li> <li>• Memories (it time allows)</li> <li>• CMOS imagers</li> <li>• Oscillators</li> <li>• Phase-locked loops</li> </ul>

	• High-speed serial I/Os
Inline resources	<a href="http://moodleucl.uclouvain.be/enrol/index.php?id=934">http://moodleucl.uclouvain.be/enrol/index.php?id=934</a>
Bibliography	Chapitres de certains livres de référence.
Faculty or entity in charge	ELEC

### Force majeure

Evaluation methods	<b>No modification except that the professors might organize an oral exam for students for whom they have doubts about the grade obtained for the written exam.</b>
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<b>Programmes containing this learning unit (UE)</b>				
Program title	Acronym	Credits	Prerequisite	Aims
Master [120] in Electrical Engineering	<a href="#">ELEC2M</a>	5		
Master [120] in Electro-mechanical Engineering	<a href="#">ELME2M</a>	5		
Master [120] in Biomedical Engineering	<a href="#">GBIO2M</a>	5		